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Sheet 1

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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of 1

Complete if Known				
Application Number	10/659,210			
Filing Date	09/09/2003			
First Named Inventor	Joseph C. Fjelstad			
Art Unit	2831			
Examiner Name	Not yet assigned			
Attorney Docket Number	ISIPI P108			

		-	U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	 	Number-Kind Code ^{2 (F known)}			9500.47
<u> </u>	<u> </u>	^{US-} 3,872,236	03-18-1975	SEWNGEL, ET AL	
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Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	Ι.
	Country Code ³ "Number ⁴ "Kind Code ⁶ (# known)	MM-DD-YYYY		Or Relevant Figures Appear	Τ°
	EP 0 150 327	08-07-1985	FEINMETTAL GMBH		
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		Country Code ³ "Number ⁴ "Kind Code ⁶ (# known)	Cite No.¹ Publication Date MM-DD-YYYY Country Code³ "Number * "Kind Code* (if known)	No.¹ Date Applicant of Cited Document MM-DD-YYYY Country Code³ 'Number ⁴ 'Kind Code¹ (# known)	Cite No.¹ Publication Date Name of Patentee or Applicant of Cited Document Where Relevant Passages Or Relevant Figures Appear Country Code³ Number ⁴ Tkind Code⁵ (# known) Publication Date Applicant of Cited Document MM-DD-YYYY

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SIPI.P108

Filing Date:

Attorney Docket No.:

First Named Inventor: Fjelstad, Joseph C.

September 9, 2003

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Application Number: 10/659,210	
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			U.	S. PATENT DOCUMENTS		
Exam. Initial*	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns,
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NON PATENT LITERATURE DOCUMENTS					
Exam. Initial*	No. ^t	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
		VOSS, BERNARD, "Routing High Bit-Rate Connectors: The physical characteristics and routing tradeoffs of using 3.125 Gb/s parts", Printed Circuit Design & Manufacture, June 2003, p. 30.			

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